

IN THE CLAIMS

- 5 1. A DC-arc suppression circuit, comprising:
 an electro-mechanical relay with a relay contact
 providing for direct current (DC) electricity to be
 controlled between a power source and an electrical load, and
 further comprising an inductive armature to open and close
10 said relay contact;
 a power transistor connected in electrical shunt
 with said relay contact and having an input for controlling a
 shunt current;
 a timing circuit connected to said inductive
15 armature and said input of the power transistor; and
 a power-control signal input connected to the
 timing circuit;
 wherein, when the timing circuit receives a command
 from the power-control signal input to interrupt a flow of
20 power from said power source to said electrical load, it
 first turns on the power transistor, then opens said relay
 contact, and lastly turns off the power transistor.
- 25 2. The DC-arc suppression circuit of claim 1, wherein:
 when the timing circuit receives a command from the
 power-control signal input to close-circuit a flow of power
 from said power source to said electrical load, it simply
 causes said relay contact to close and does not operate the
 power transistor.

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3. The DC-arc suppression circuit of claim 1, wherein:
the power transistor is a MOSFET-type with its
drain and source electrodes connected in parallel to said
relay contact.

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4. The DC-arc suppression circuit of claim 1, wherein:
the timing circuit is such that it includes a
switch transistor to electrically control said inductive
armature.

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5. The DC-arc suppression circuit of claim 1, wherein:
the timing circuit is such that it provides about a
two millisecond delay between a signal at the power-control
signal input and its resulting operation of the relay.

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6. The DC-arc suppression circuit of claim 1, wherein:
the timing circuit is such that it provides about a
twenty millisecond long switch-ON pulse to the power
transistor beginning at the arrival of an OFF-command signal
at the power-control signal input.

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7. The DC-arc suppression circuit of claim 1, wherein:
the power transistor is a MOSFET-type with its
drain and source electrodes connected in parallel to said
relay contact; and

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the timing circuit is such that it includes a
switch transistor to electrically control said inductive
armature, and it provides about a two millisecond delay
between a signal at the power-control signal input and its
resulting operation of the relay, and it further provides
about a twenty millisecond long switch-ON pulse to the power

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transistor beginning at the arrival of an OFF-command signal at the power-control signal input.

8. A remote power controller, comprising:

5 a network client for sending and receiving power status and power control messages over a computer data network;

 an electro-mechanical relay with a relay contact providing for direct current (DC) electricity to be
10 controlled between a power source and an electrical load, and further comprising an inductive armature to open and close said relay contact;

 a power transistor connected in electrical shunt with said relay contact and having an input for controlling a
15 shunt current;

 a timing circuit connected to receive a decoded power-ON command and a power-OFF command from the network client; and

 wherein, when the timing circuit receives said
20 power-OFF command to interrupt a flow of power from said power source to said electrical load, it first turns on the power transistor, then opens said relay contact, and then turns the power transistor back off.

25 9. The remote power controller of claim 8, wherein:

 when the timing circuit receives a command from the power-control signal input to close-circuit a flow of power from said power source to said electrical load, it simply causes said relay contact to close and does not operate the
30 power transistor.

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wherein, any tendency of said pair of contacts in said electro-mechanical relay to arc when being opened is suppressed.

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